



## PCI-SIG ENGINEERING CHANGE NOTICE

<b>TITLE:</b>	+3Vaux Typo correction
<b>DATE:</b>	January <u>12</u> , 200 <u>4</u>
<b>AFFECTED DOCUMENT:</b>	PCI Express Mini Card, Release 1.0
<b>SPONSOR:</b>	Ron Shaw, Dell, Inc.

### **Part I**

#### **1. Summary of the Functional Changes**

1. Correct typo in section 3.2.1 left after addition of +3Vaux pin

#### **2. Benefits as a Result of the Changes**

Correct error in the specification to match text found elsewhere.

#### **3. Assessment of the Impact**

None.

#### **4. Analysis of the Hardware Implications**

N/A.

#### **5. Analysis of the Software Implications**

N/A.

**Part II**

**Detailed Description of the change**

*Chage section 3.2.1 to the following:*

**3.2.1 Power Sources and Grounds**

PCI Express Mini Card provides ~~three~~ power sources: ~~two~~ at 3.3V (+3.3V ~~and +3.3Vaux~~) and one at 1.5V (+1.5V). The auxiliary voltage source (+3.3Vaux) ~~may be the only supply voltage~~ available during the system’s stand-by/suspend state to support wake event processing on the communications card.

**Deleted:** two

**Deleted:** one

**Deleted:** is sourced over the same pins as the primary voltage (+3.3V) and is